## FIRST INTERNATIONAL COMPUTER, INC.

## PA-2002

**Processor** Pentium

**Processor Speed** 75/90/100/120/133/150/166MHz

Chip Set VIA

Max. Onboard DRAM 128MB

**Cache** 256/512/1024KB

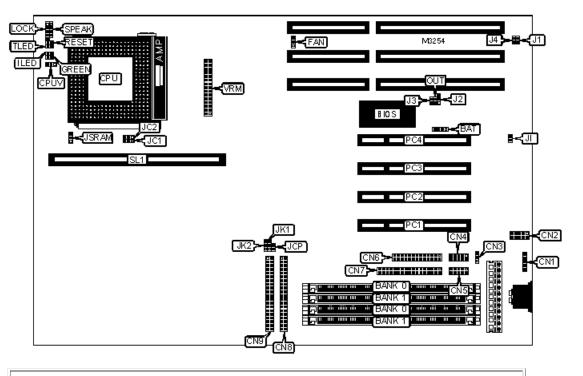
**BIOS** Award

**Dimensions** 274mm x 218mm

I/O Options 32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2

mouse interface, serial ports (2), VRM connector, cache slot, IR connector

NPU Options None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	BAT	Green PC connector	GREEN
PS/2 mouse interface	CN1	IR LED	ILED
IR connector	CN2	Power LED & keylock	LOCK
Serial port 2	CN4	Reset switch	RESET
Serial port 1	CN5	Speaker	SPEAK
Parallel port	CN6	Turbo LED	TLED

Floppy drive interface	CN7	32-bit PCI slots	PC1 - PC4
IDE interface 2	CN8	Cache slot	SL1
IDE interface 1	CN9	VRM connector	VRM
Chassis fan power	FAN		

## **USER CONFIGURABLE SETTINGS**

	Function	Jumper	Position
»	Factory configured - do not alter	CN3	N/A
»	Password disabled	JCP	Open
	Password enabled	JCP	Closed
»	Parallel port unidirectional	JI	Open
	Parallel port bidirectional	JI	Closed
	BIOS select Intel 28F001BX-T	J2	Pins 2 & 3 closed
	BIOS select SST 29EE010	J2	Pins 1 & 2 closed
»	Turbo LED enabled	J3	Open
	Green PC LED enabled	J3	Closed
»	Factory configured - do not alter	OUT	N/A

DRAM CONFIGURATION				
Size Bank 0		Bank 1		
2MB	(2) 256K x 36	None		
4MB	(2) 256K x 36	(2) 256K x 36		
8MB	(2) 1M x 36	None		
10MB	(2) 1M x 36	(2) 256K x 36		
16MB	(2) 1M x 36	(2) 1M x 36		
18MB	(2) 2M x 36	(2) 256K x 36		

32MB	(2) 4M x 36	None		
34MB	(2) 4M x 36	(2) 256K x 36		
36MB	(2) 4M x 36	(2) 512K x 36		
40MB	(2) 4M x 36	(2) 1M x 36		
48MB	(2) 4M x 36	(2) 2M x 36		
64MB	(2) 4M x 36	(2) 4M x 36		
66MB	(2) 8M x 36	(2) 256K x 36		
72MB	(2) 8M x 36	(2) 1M x 36		
96MB	(2) 8M x 36	(2) 4M x 36		
128MB	(2) 8M x 36	(2) 8M x 36		
Note: Board accepts EDO DRAM.				

CACHE CONFIGURATION		
Size SL1		
256KB	Installed	
512KB	Installed	
1MB (asynchronous only)	Installed	

CACHE JUMPER CONFIGURATION			
Туре	JSRAM		
Asynchronous	Pins 1 & 2 closed		
Synchronous	Pins 2 & 3 closed		

CPU SPEED CONFIGURATION				
Speed	JC1	JC2	JK1	JK2
75MHz	Pins 2 & 3 closed			

90MHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
100MHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
120MHz	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
133MHz	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
150MHz	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
166MHz	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed

	CPU VOLTAGE CONFIGURATION		
Voltage		CPUV	
<b>»</b>	3.384v	Pins 2 & 3 closed	
	3.54v	Pins 1 & 2 closed	

	POWER SUPPLY CONFIGURATION				
Setting		J1	J4		
<b>»</b>	Normal power supply	Closed	Closed		
	Software power supply	Open	Open		