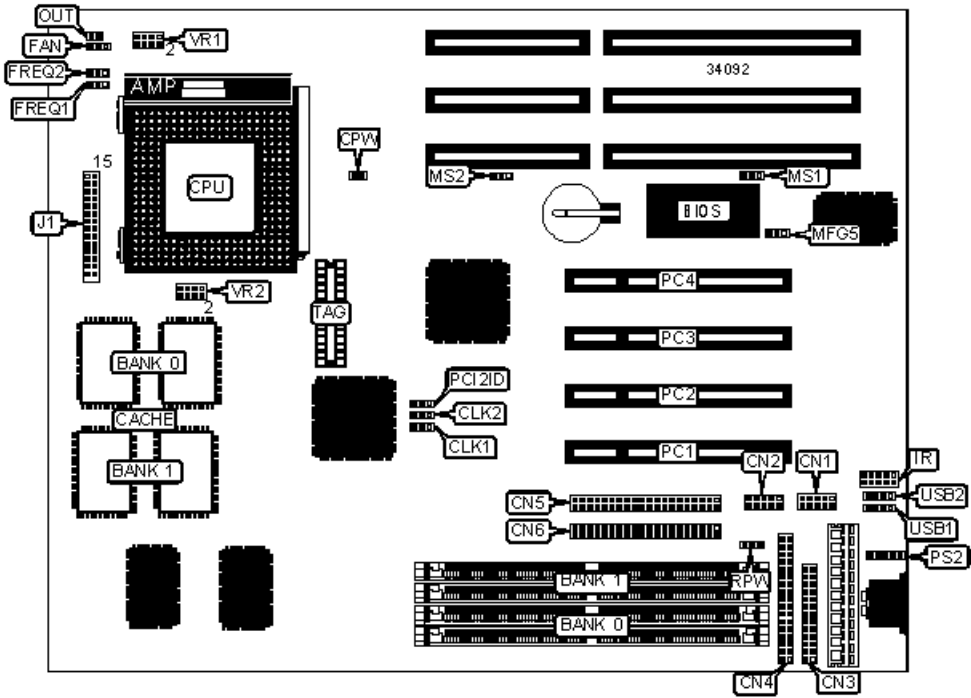


FIRST INTERNATIONAL COMPUTER, INC.

PA-2005 (REV. A1)

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/200MHz
Chip Set	VIA
Video Chip Set	None
Maximum Onboard Memory	512MB (EDO supported)
Maximum Video Memory	None
Cache	256/512/1024KB
BIOS	Award
Dimensions	254mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connector, USB connectors (2), remote control power
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN1	Green PC LED	J1 pins 13 & 14
Serial port 2	CN2	Speaker	J1 pins 15 - 18
Parallel port	CN3	IDE interface LED	J1 pins 20 & 21

Floppy drive interface	CN4	Remote control power	J1 pins 23 & 24
IDE interface 1	CN5	Reset switch	J1 pins 27 & 28
IDE interface 2	CN6	32-bit PCI slots	PC1 – PC4
Chassis fan power	FAN	Outlet connector	OUT
IR connector	IR	PS/2 mouse interface	PS2
Power LED & keylock	J1 pins 1 - 5	Remote control power	RPW
Turbo LED	J1 pins 7 & 8	USB connector 1	USB1
Green PC connector	J1 pins 10 & 11	USB connector 2	USB2

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Password disabled	CPW	Open
	Password enabled	CPW	Closed
»	Flash BIOS select SST 29EE010	MFG5	Pins 2 & 3 closed
	Flash BIOS select 28F001BX-T	MFG5	Pins 1 & 2 closed
»	More than 1 PCI card installed before PCI Encoding Standard	PCI2ID	Pins 1 & 2 closed
	1 PCI card installed before PCI Encoding Standard	PCI2ID	Pins 2 & 3 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
40MB	(2) 4M x 36	(2) 1M x 36

48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
512MB	(2) 32M x 36	(2) 32M x 36
Note: Board accepts EDO memory.		

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB	(2) 32K x 32	None	(1) 16K/32K x 8
512KB	(2) 64K x 32	None	(1) 16K/32K x 8
512KB	(2) 32K x 32	(2) 32K x 32	(1) 16K/32K x 8
1MB	(2) 64K x 32	(2) 64K x 32	(1) 32K x 8

CPU SPEED SELECTION (CYRIX)						
CPU speed	Clock speed	Multiplier	CLK1	CLK2	FREQ1	FREQ2
120MHz	50MHz	2x	2 & 3	2 & 3	2 & 3	1 & 2
133MHz	55MHz	2x	1 & 2	1 & 2	2 & 3	1 & 2

150MHz	60MHz	2x	2 & 3	1 & 2	2 & 3	1 & 2
166MHz	66MHz	2x	1 & 2	2 & 3	2 & 3	1 & 2
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (AMD)						
CPU speed	Clock speed	Multiplier	CLK1	CLK2	FREQ1	FREQ2
75MHz	50MHz	1.5x	2 & 3	2 & 3	1 & 2	1 & 2
90MHz	60MHz	1.5x	2 & 3	1 & 2	1 & 2	1 & 2
100MHz	66MHz	1.5x	1 & 2	2 & 3	1 & 2	1 & 2
120MHz	60MHz	1.5x	2 & 3	1 & 2	1 & 2	1 & 2
133MHz	66MHz	1.5x	1 & 2	2 & 3	1 & 2	1 & 2
150MHz	60MHz	2x	2 & 3	1 & 2	2 & 3	1 & 2
166MHz	66MHz	2x	1 & 2	2 & 3	2 & 3	1 & 2
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (INTEL)						
CPU speed	Clock speed	Multiplier	CLK1	CLK2	FREQ1	FREQ2
75MHz	50MHz	1.5x	2 & 3	2 & 3	1 & 2	1 & 2
90MHz	60MHz	1.5x	2 & 3	1 & 2	1 & 2	1 & 2
100MHz	66MHz	1.5x	1 & 2	2 & 3	1 & 2	1 & 2
120MHz	60MHz	2x	2 & 3	1 & 2	2 & 3	1 & 2
133MHz	66MHz	2x	1 & 2	2 & 3	2 & 3	1 & 2
150MHz	60MHz	2.5x	2 & 3	1 & 2	2 & 3	2 & 3
166MHz	66MHz	2.5x	1 & 2	2 & 3	2 & 3	2 & 3
200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3
Note: Pins designated should be in the closed position.						

CPU VOLTAGE SELECTION (SINGLE)

Voltage	VR1	VR2
3.384v	Pins 1 & 2 closed	Pins 1 & 2, 3 & 4 closed
3.4v – 3.6v	Pins 3 & 4 closed	Pins 1 & 2, 3 & 4 closed

CPU VOLTAGE SELECTION (DUAL)

Voltage	V core	VR1	VR2
3.3v	2.5v	7 & 8	5 & 6, 7 & 8
3.3v	2.7v	5 & 6	5 & 6, 7 & 8
3.3v	2.8v	5 & 6	5 & 6, 7 & 8
3.3v	2.9v	5 & 6	5 & 6, 7 & 8

Note: Pins designated should be in the closed position.

PS/2 MOUSE SELECTION

Setting	MS1	MS2
» PS/2 mouse IRQ disabled	Pins 2 & 3 closed	Pins 1 & 2 closed
PS/2 mouse IRQ enabled	Pins 1 & 2 closed	Pins 2 & 3 closed